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PATENT DOCKET NO. 30585/4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.
Serial No.: 09/348,317 Art Unit: 2784
Filed: July 9, 1999 Examiner: unassigned
Title: RECORDED CLASSIFICATION OF INSTRUCTIONS EXECUTED BY A COMPUTER

COMMISSIONER FOR PATENTS
Washington D.C. 20231

PRELIMINARY AMENDMENT AND TRANSMITTAL OF FORMAL DRAWINGS

Kindly amend the application as follows.

In the Specification:

Please amend the specification as follows.

At page 4, line 31, change "schedules" to -- scheduled --.

At page 5, line 2, change "a one" to --one--.

At page 7, line 28, change "which at which at" to --which at--.

At page 9, line 21-22, change "for translating address references are" to --translates address references--.

At page 9, line 28, change "Preferred embodiments" to --Embodiments--.

At page 11, line 14, change "Preferred embodiments" to --Embodiments--.

At page 12, line 6, add a period at the end of the line.

At page 13, line 21, change "Preferred embodiments" to --Embodiments--.

At page 19, lines 20-21, change "the I-cache" to --I-cache **112**--.

At page 24, lines 12-17, replace the sentence "Translator 124 … data)." with the following: --Translator **124** also assumes that all memory references are to well-behaved memory. ("Well-behaved memory" is a memory from which a load will receive the data last stored at the memory location. Non-well -behaved memory is typified by memory-mapped device controllers, also called "I/O space," where a read causes the memory to change state, or where a read does not necessarily return the value most-recently written, or two successive reads return distinct data.)--.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on December 15, 2000 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Washington D.C. 20231.

Joel E. Boaz